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circuit to delay the input signal by a second period as a function of the analog control signal amplitude.

Re
9. (Amended) A device, comprising:

a delay lock loop circuit responsive to a first input signal to delay the first input signal by a first period and to generate an analog control signal having an amplitude; and

a delay circuit coupled to the delay lock loop circuit and responsive to a second input signal, the delay circuit being responsive to the analog control signal from the delay lock loop circuit to delay the second signal by a second period as a function of the analog control signal amplitude.

Re
14. (Amended) A method, comprising:

receiving a first signal and a second signal;

using a delay lock loop circuit to delay the first signal by a first period;

controlling the first period as a function of an analog control signal having an amplitude; and

using a delay circuit to delay the second signal by a second period in response to the analog control signal amplitude from the delay lock loop circuit. --
